

FIG. 1

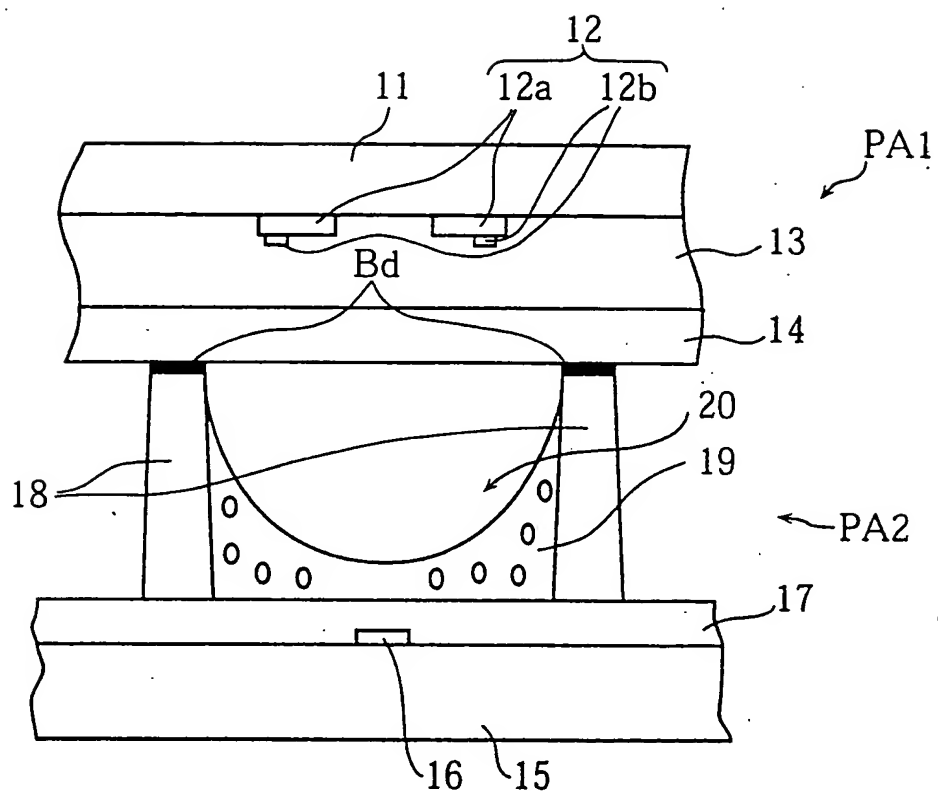




FIG. 2

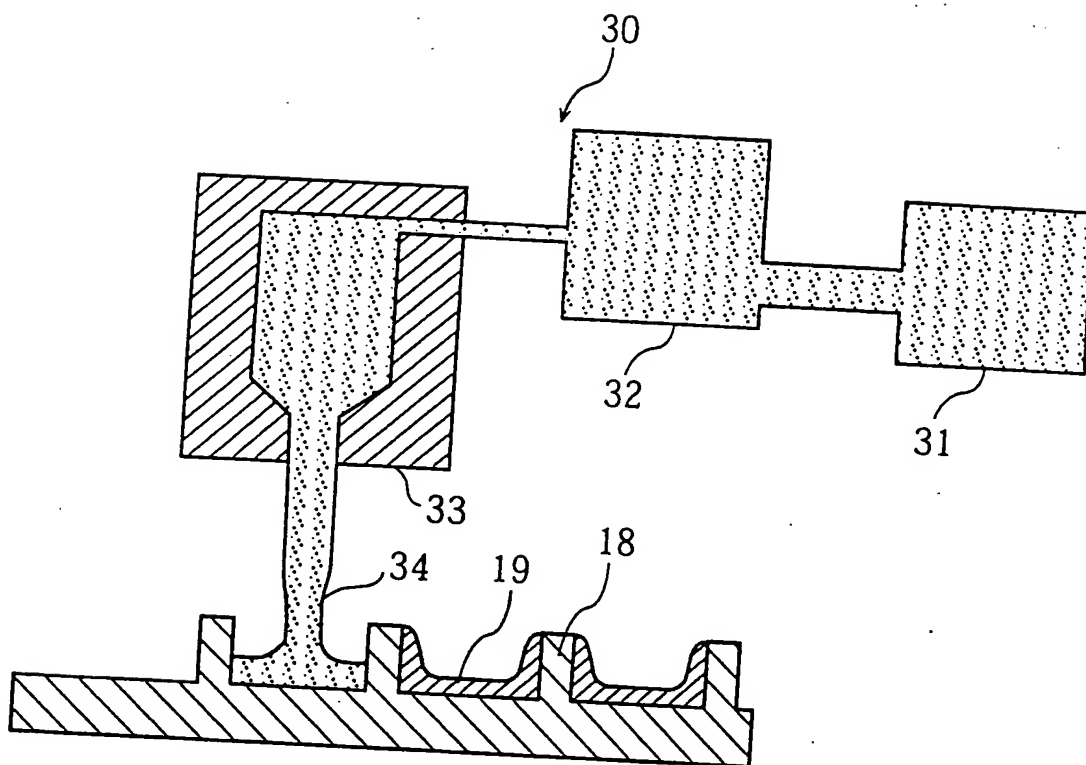




FIG. 3

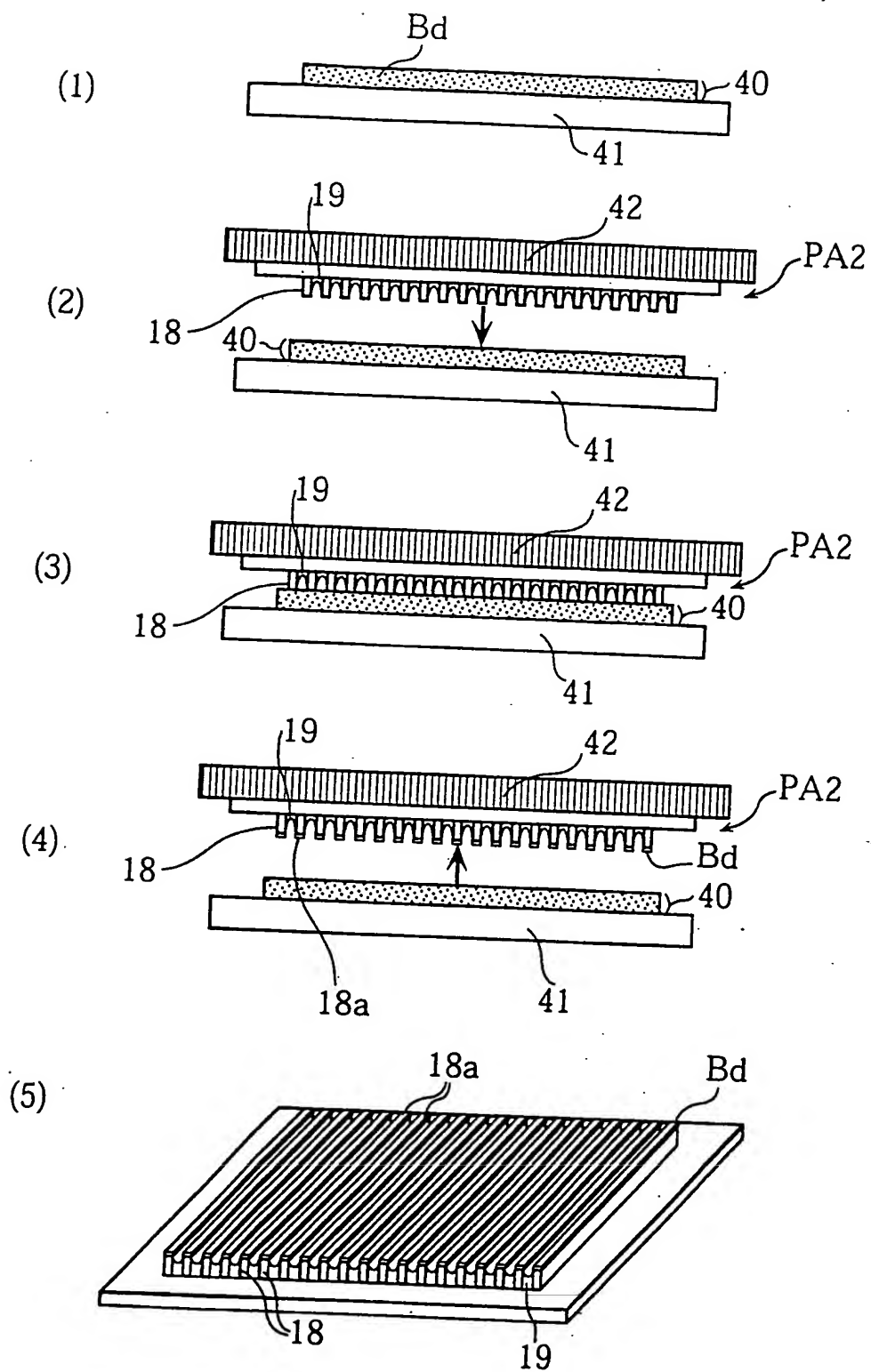




FIG. 4A

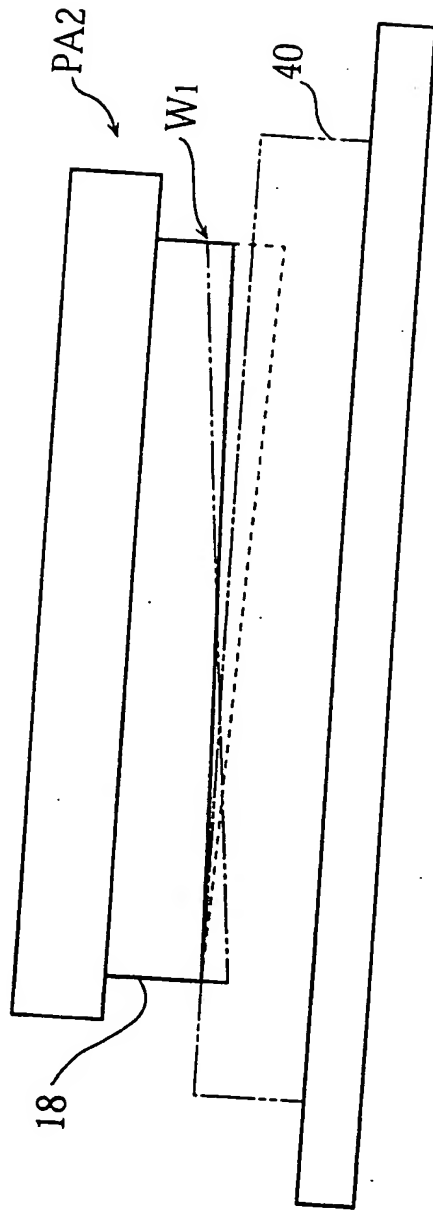


FIG. 4B

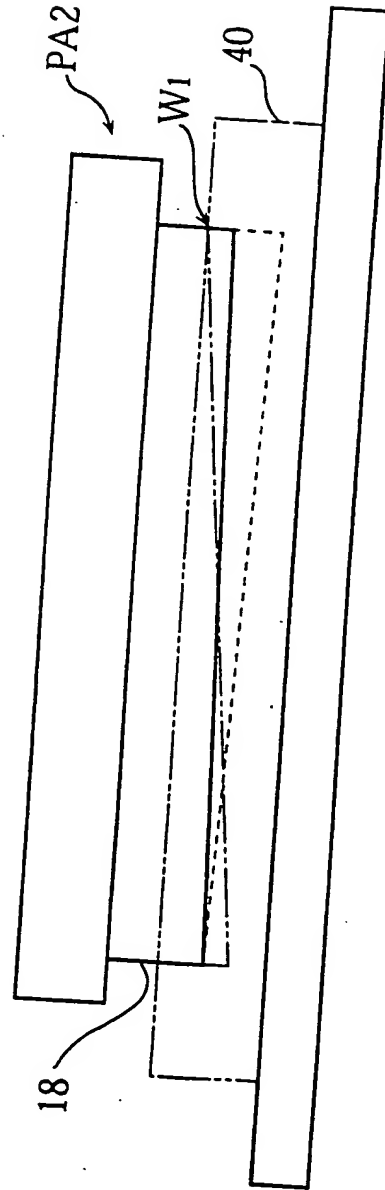




FIG. 5

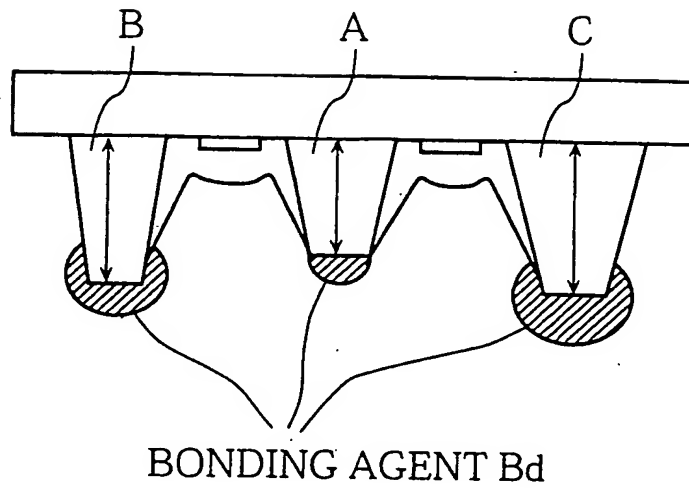


FIG. 6A

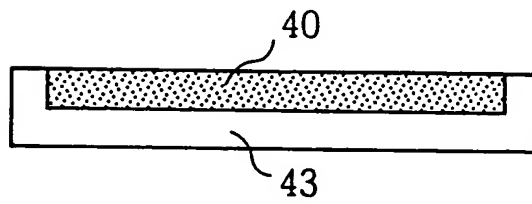


FIG. 6B

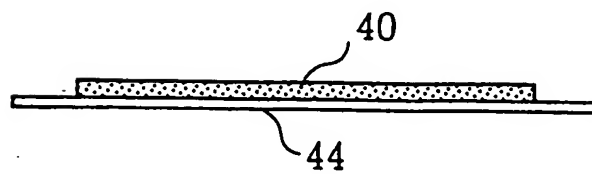




FIG. 7

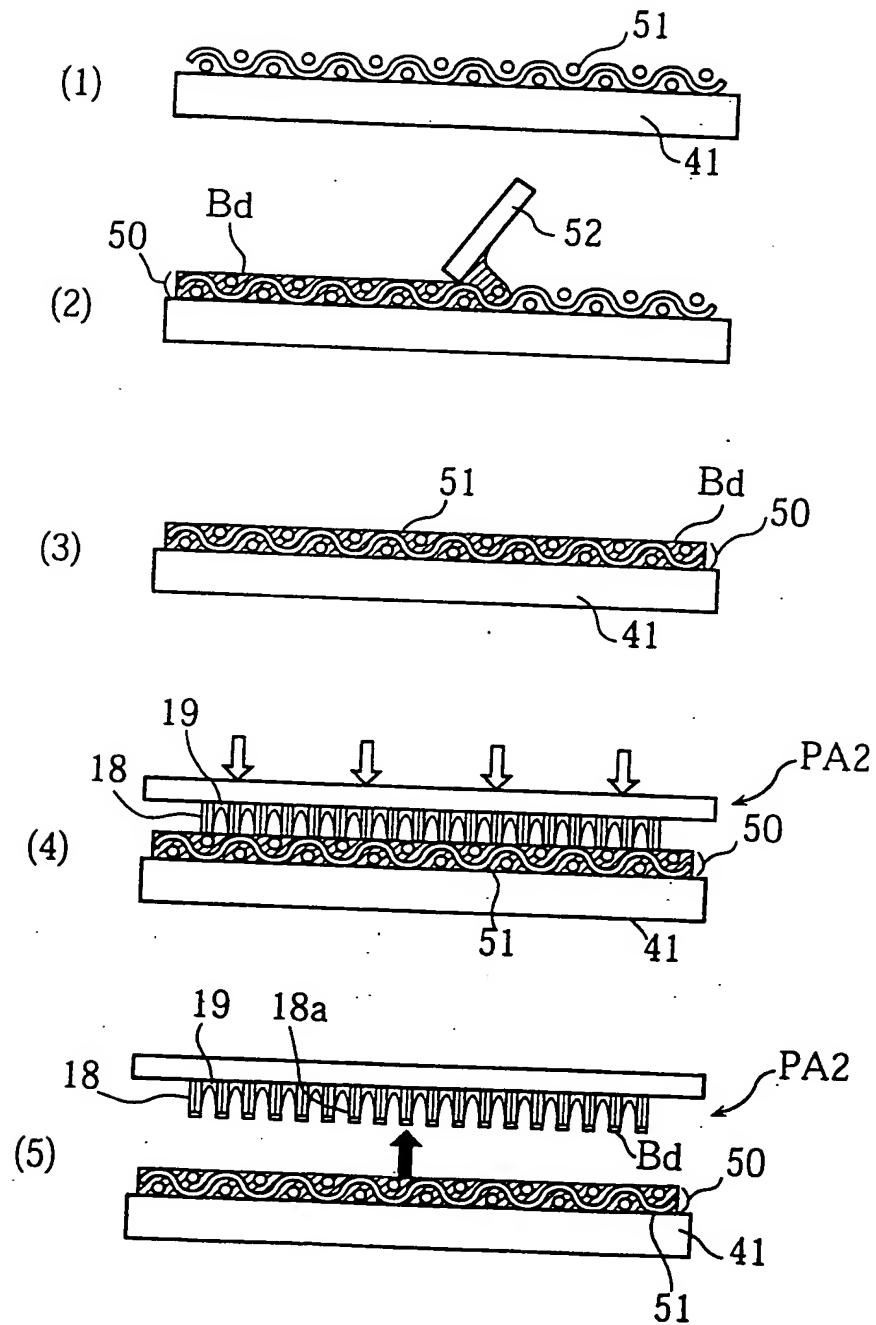




FIG. 8

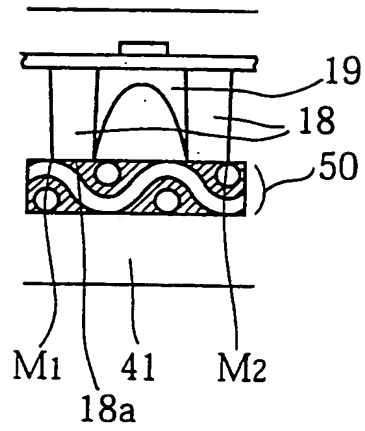


FIG. 9

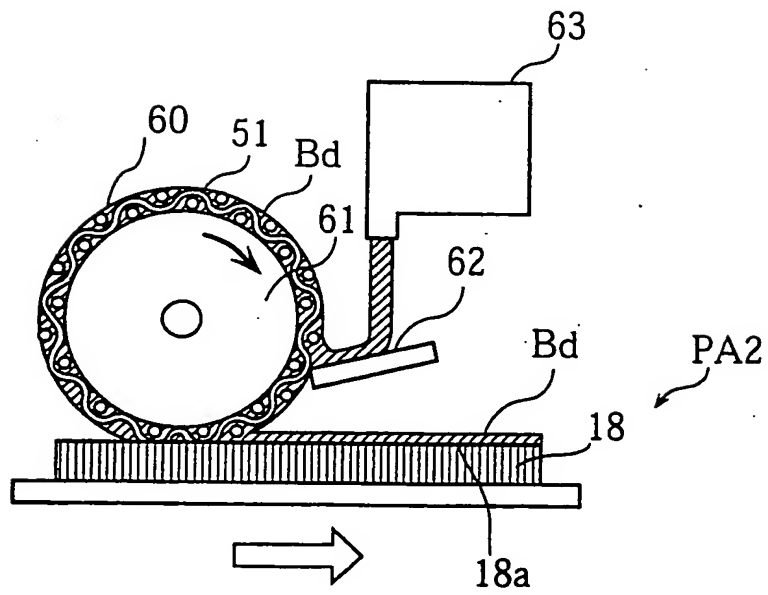




FIG. 10

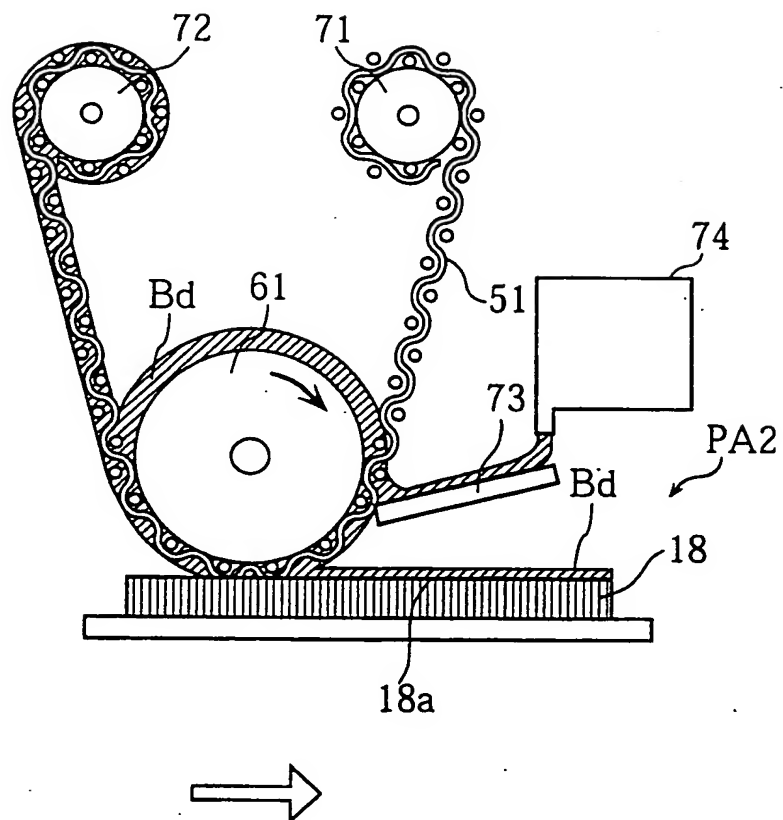




FIG. 11

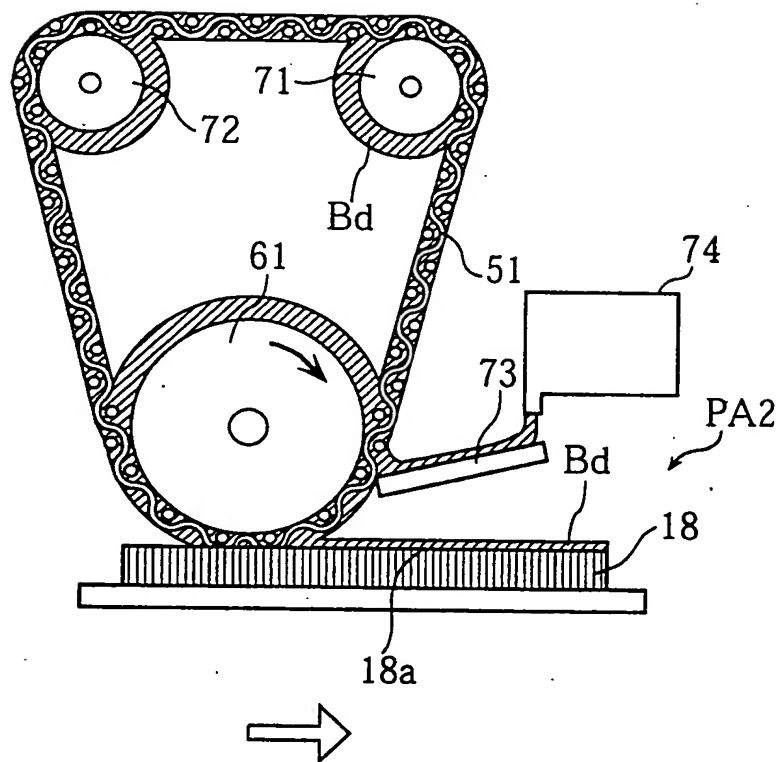




FIG. 12

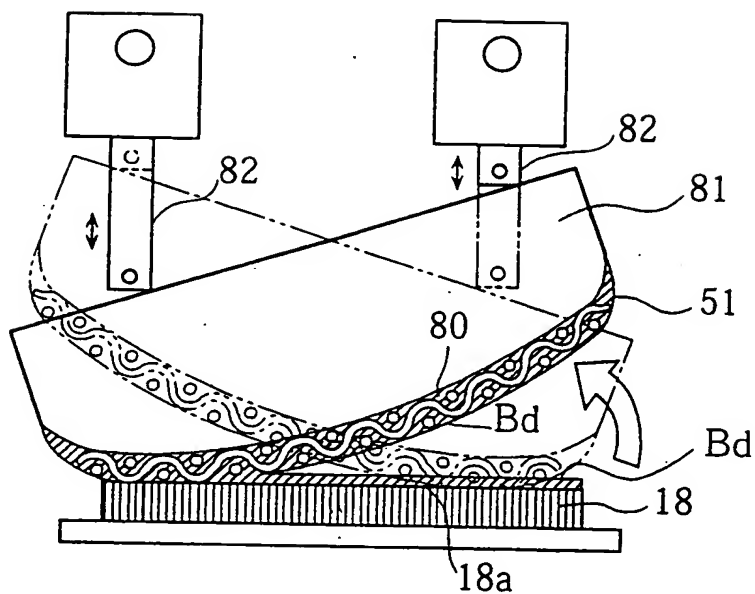
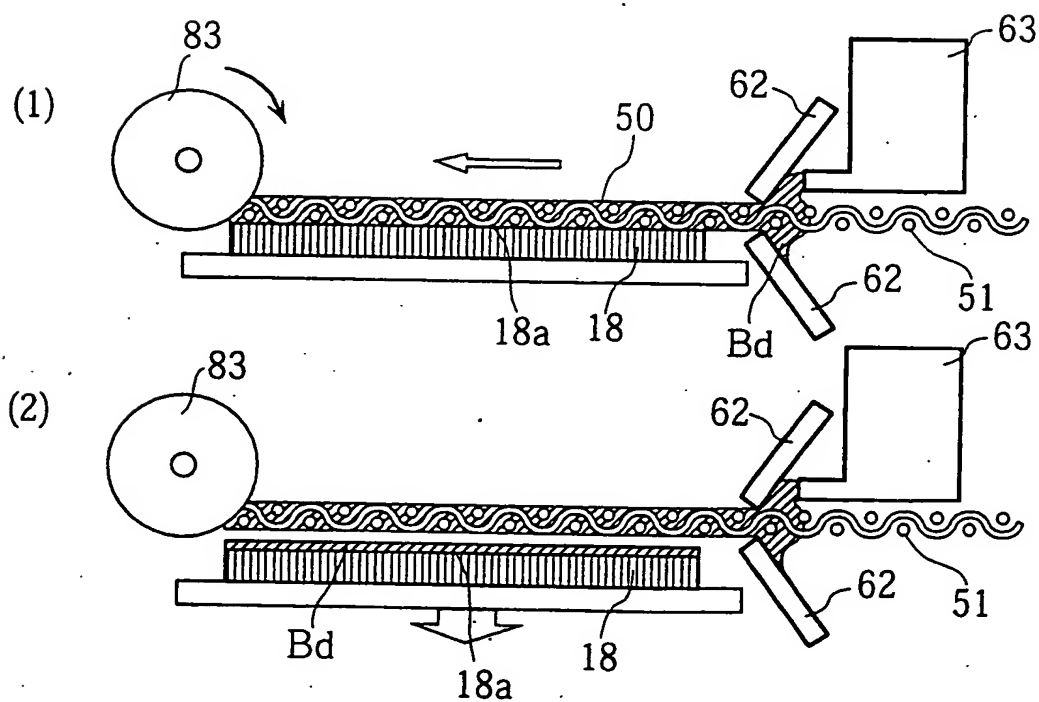


FIG. 13





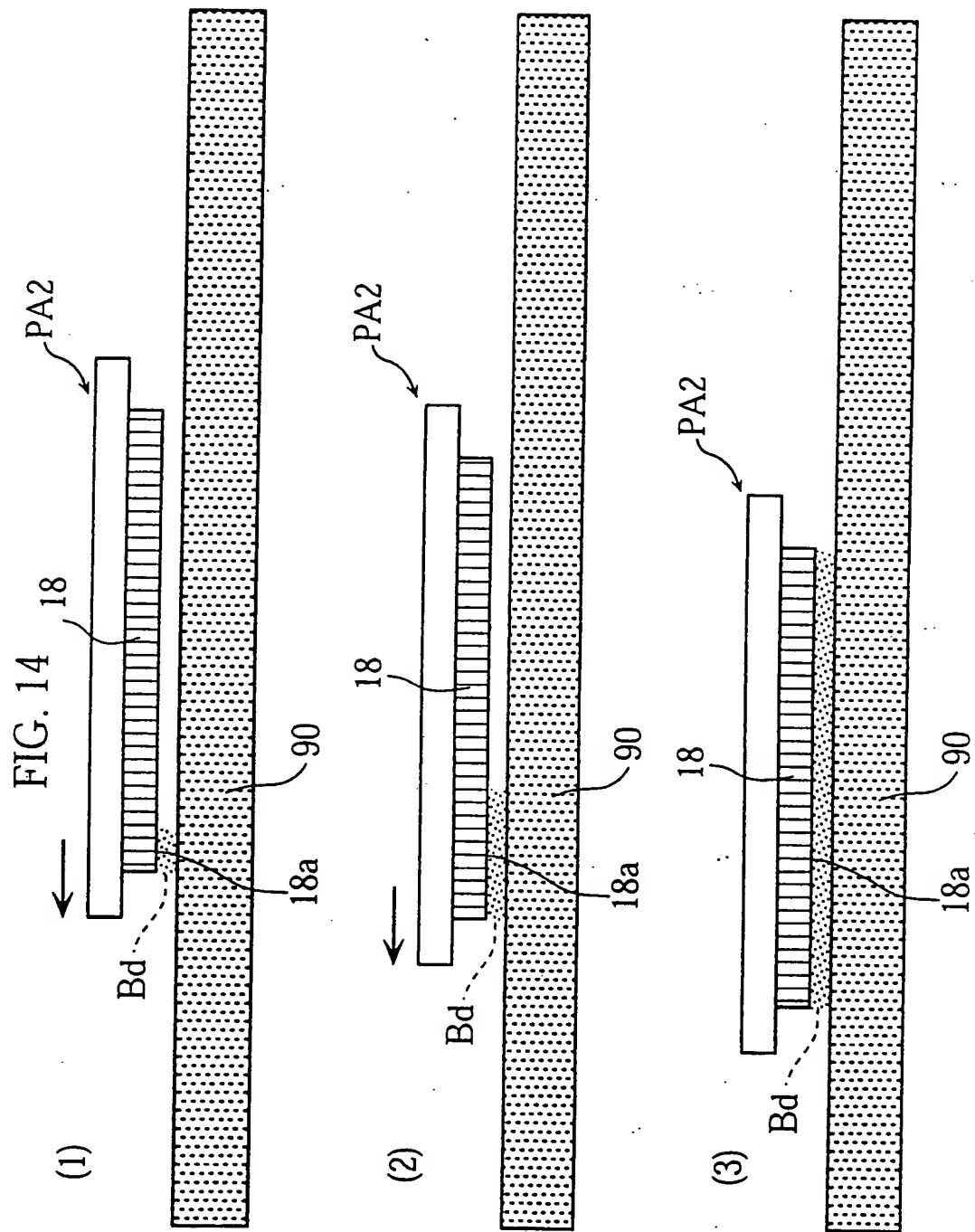




FIG. 15

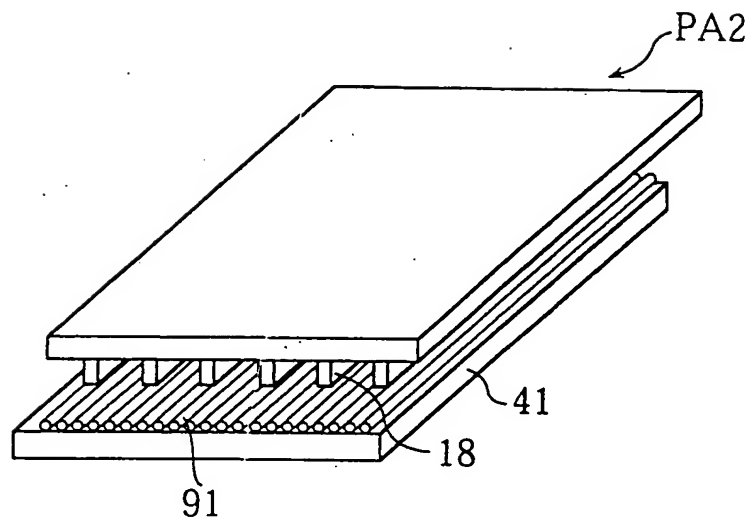


FIG. 16

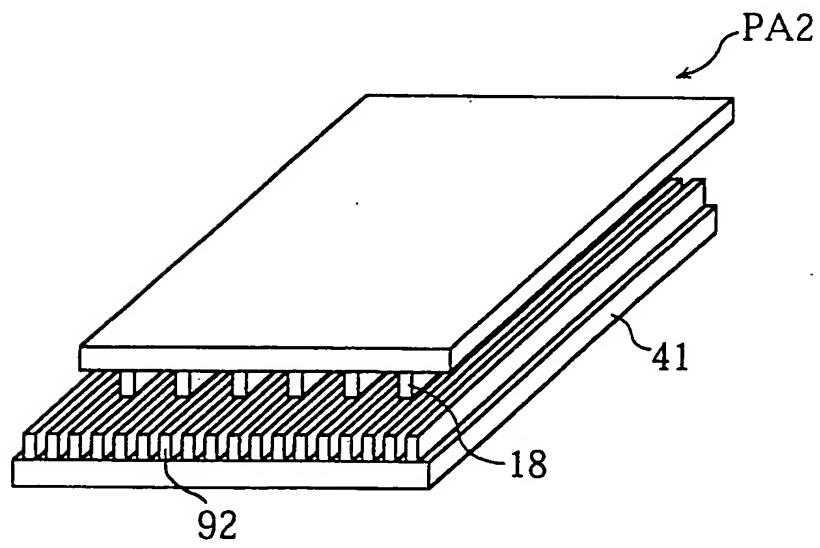




FIG. 17

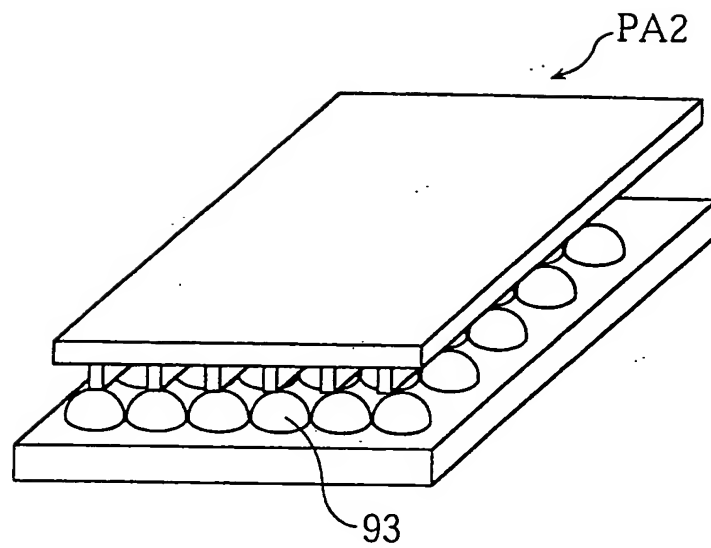


FIG. 18

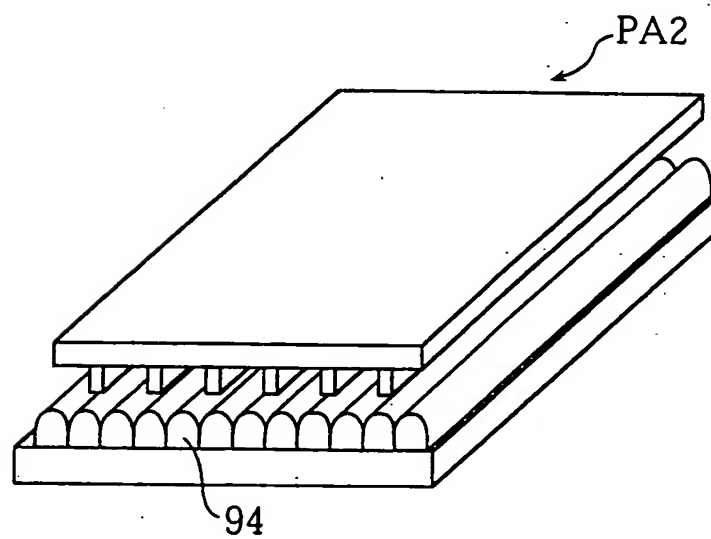




FIG. 19

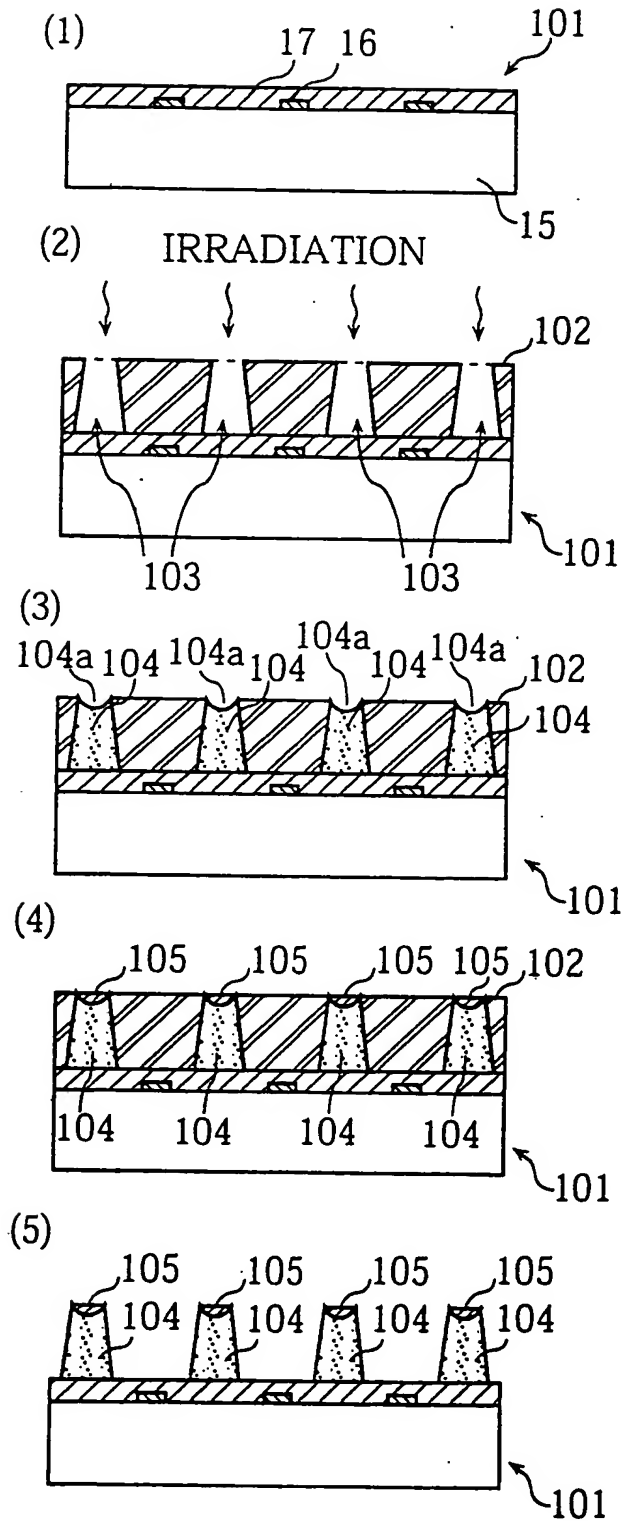




FIG. 20

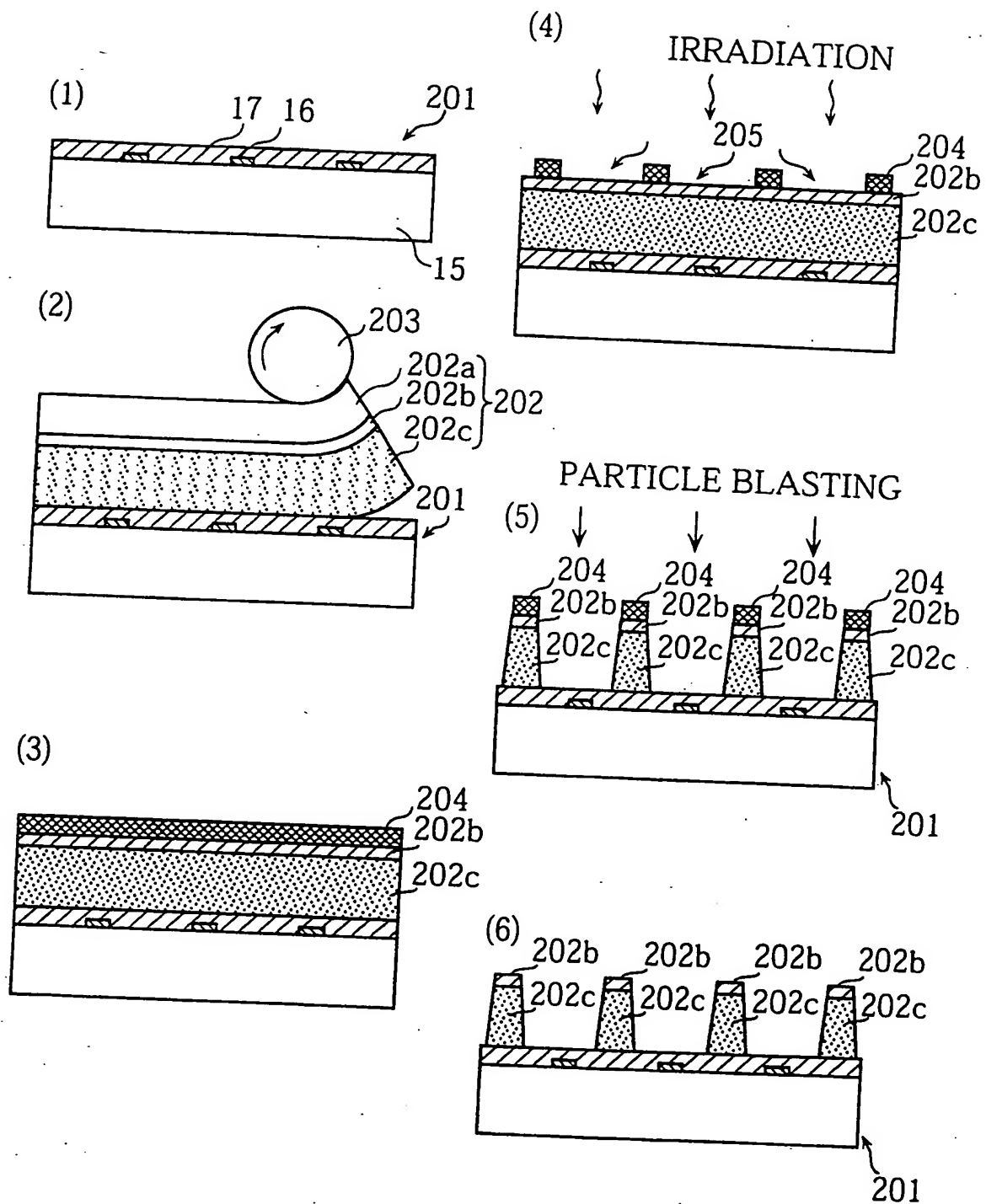




FIG. 21

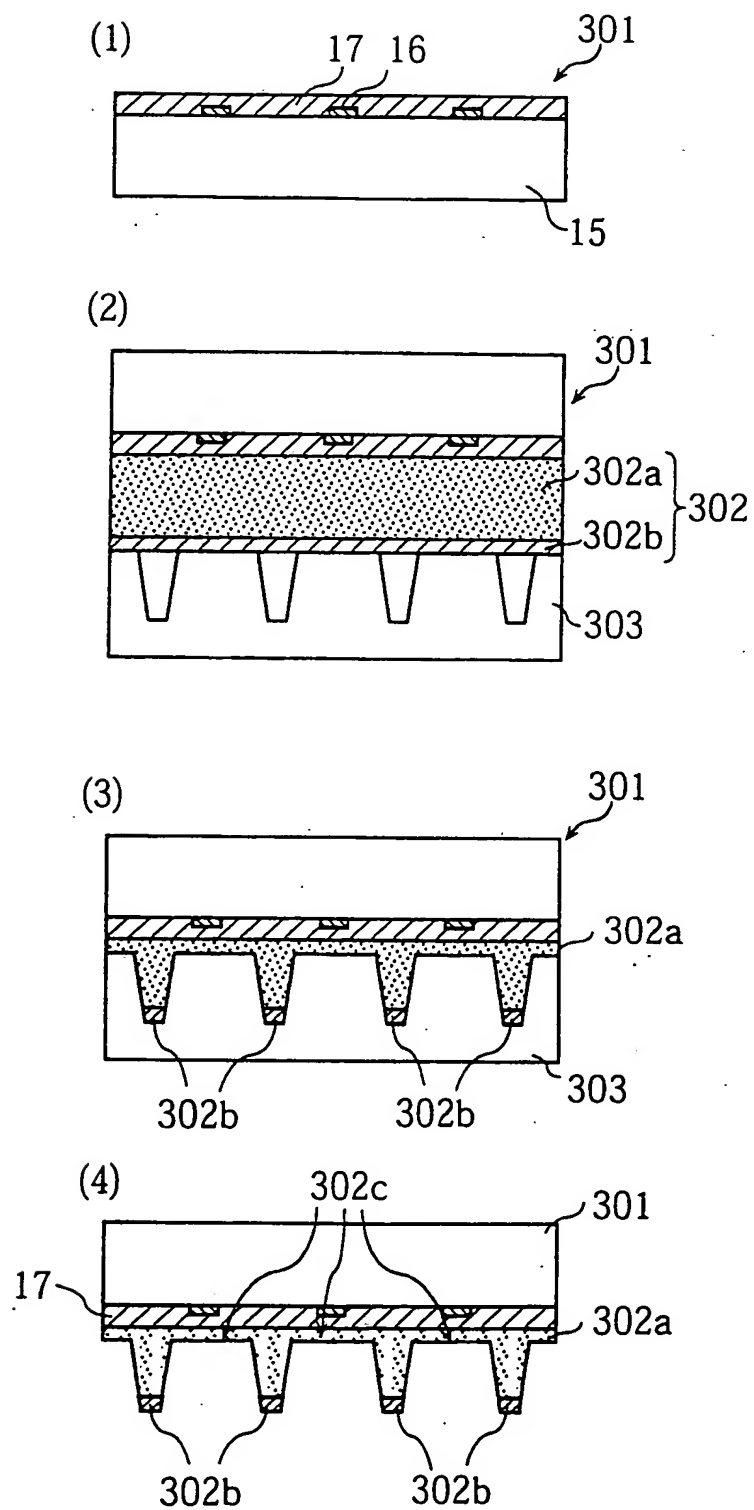




FIG. 22

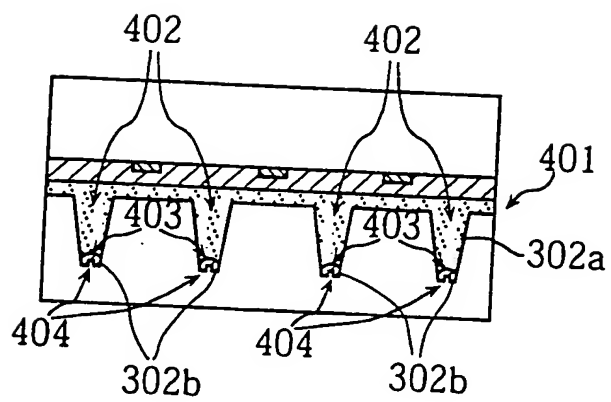




FIG. 23

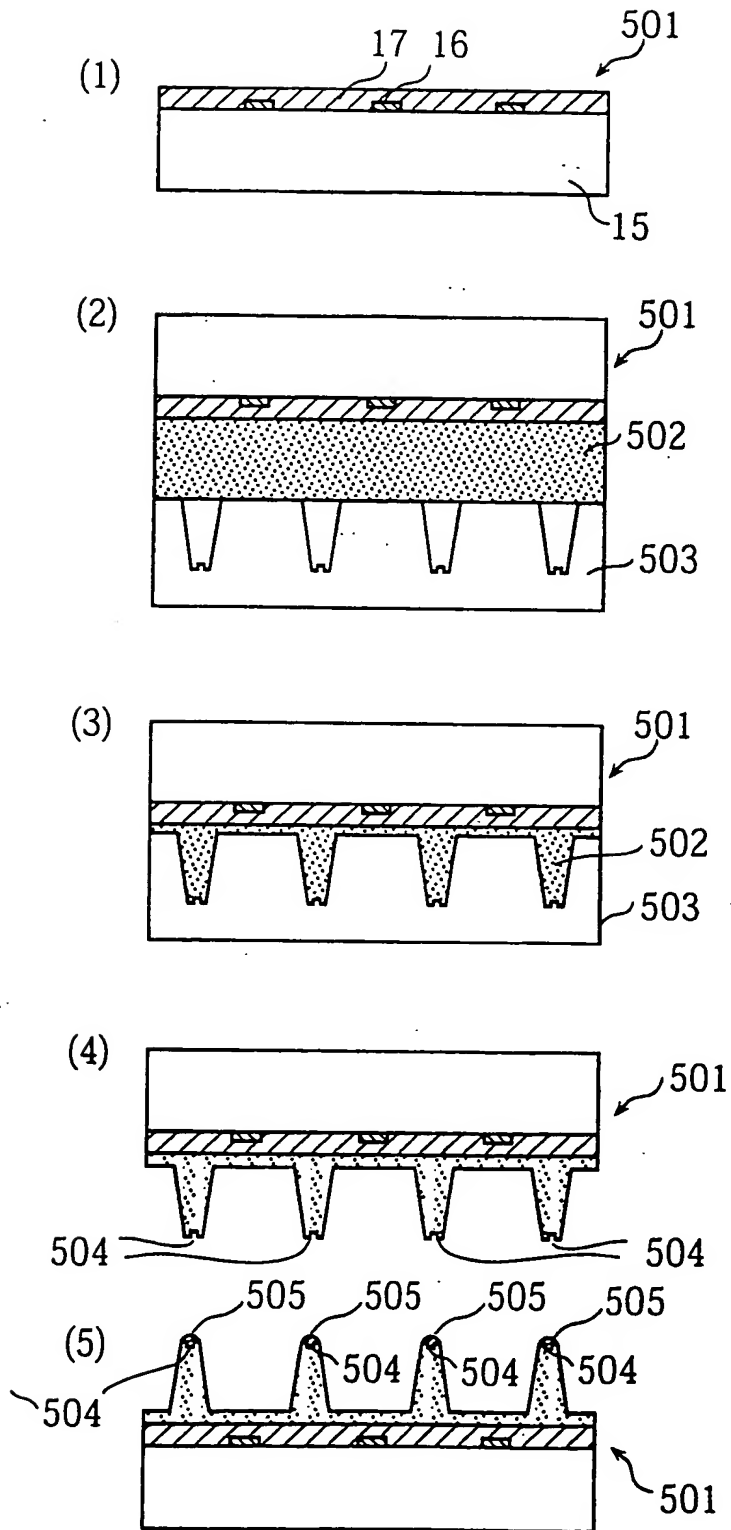




FIG. 24

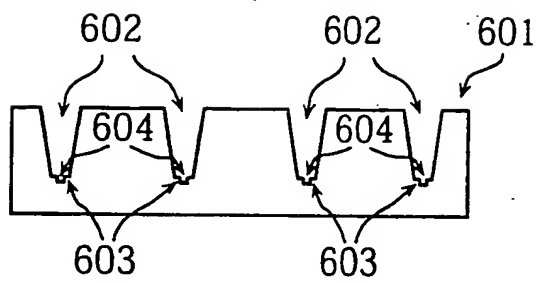




FIG. 25

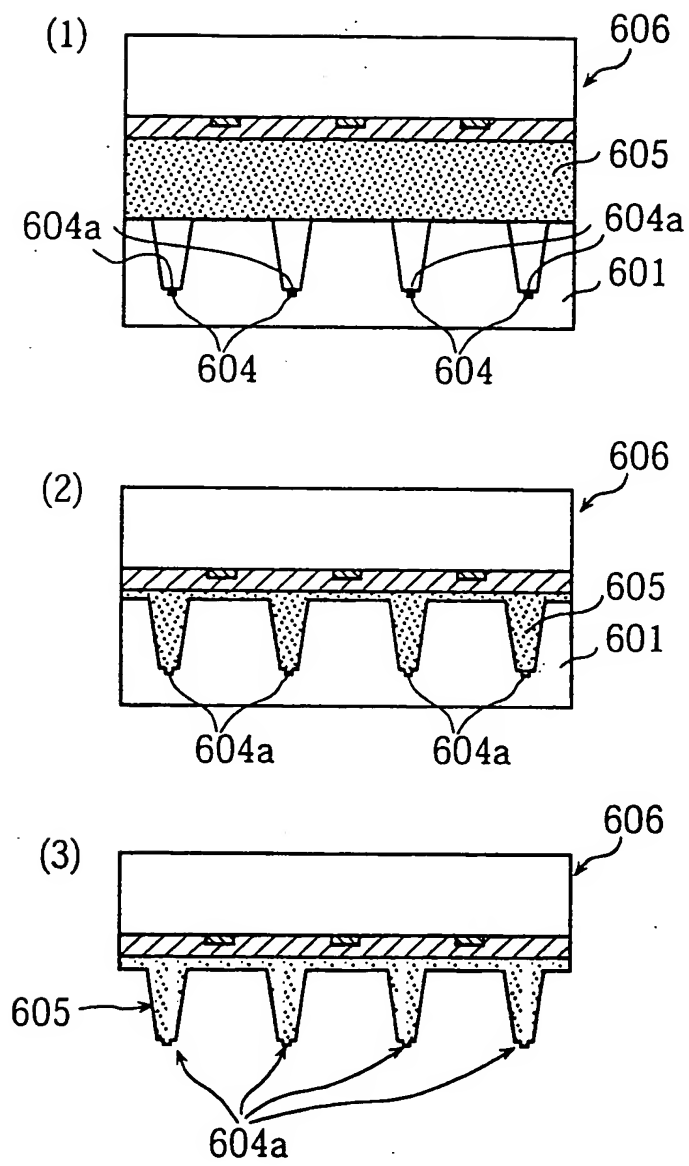




FIG. 26

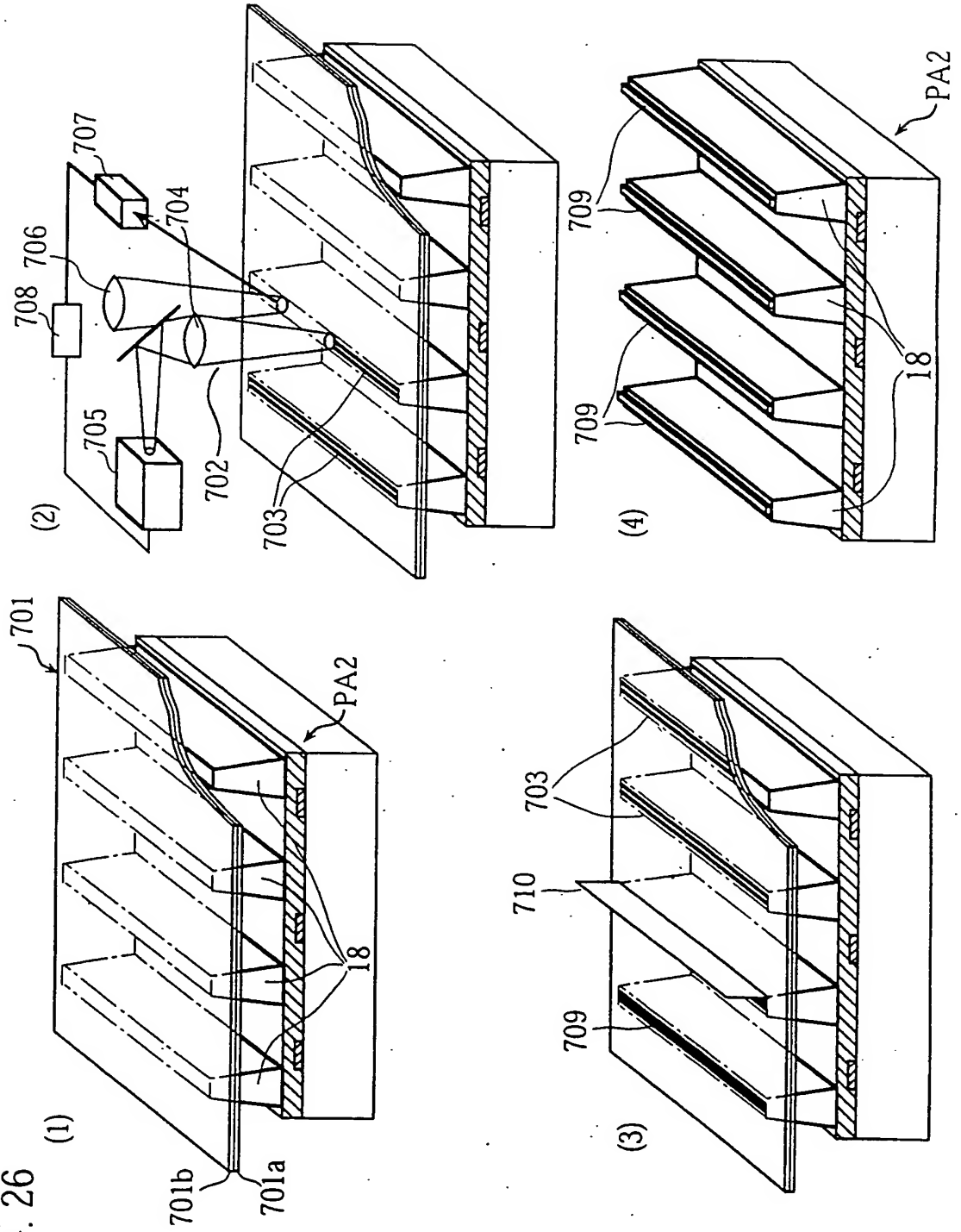




FIG. 27

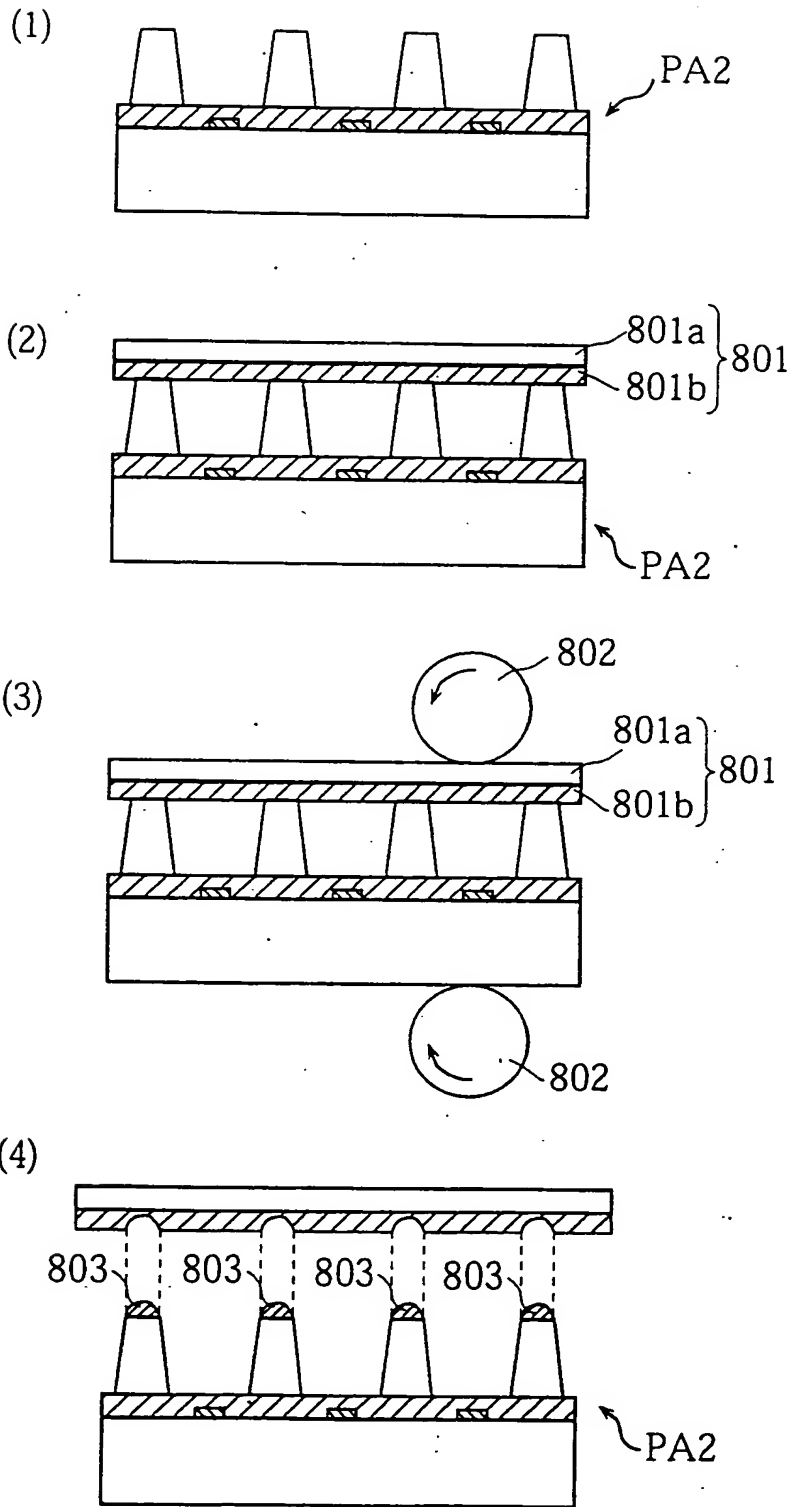




FIG. 28

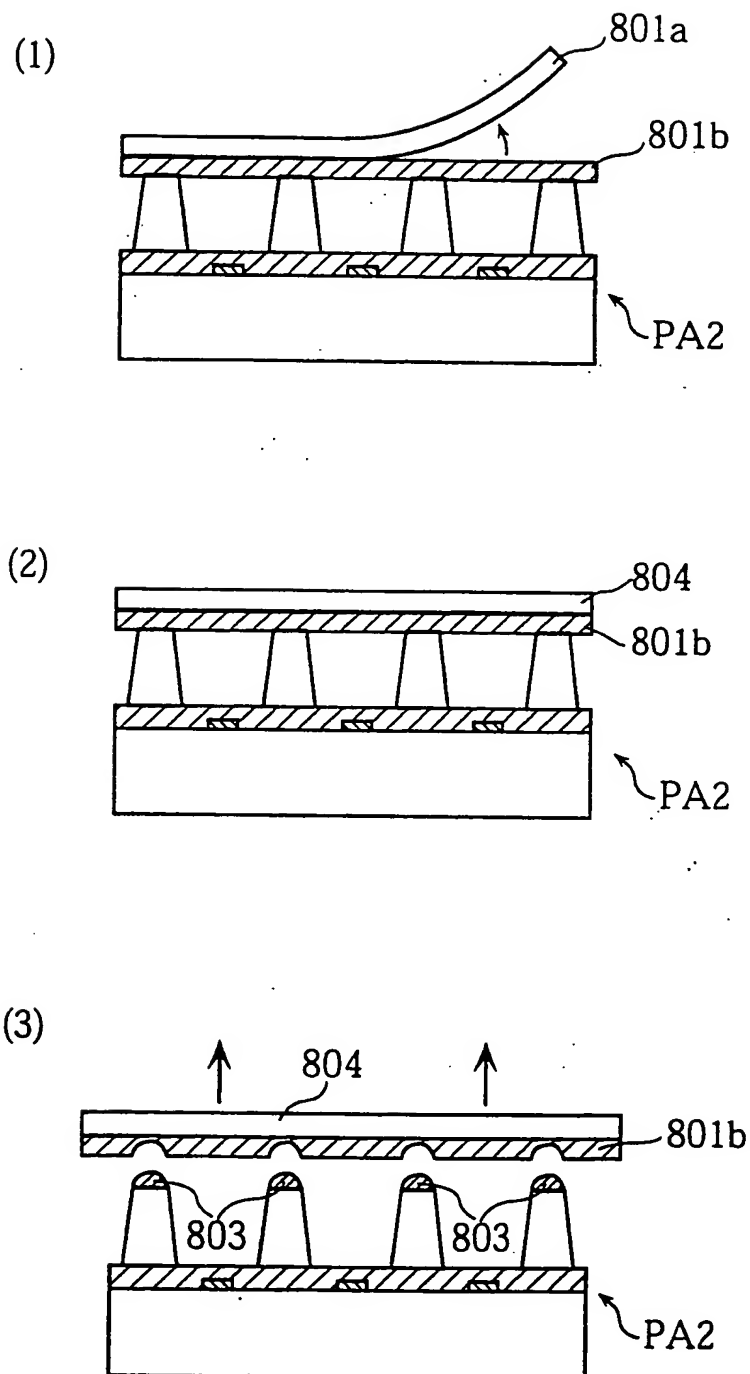




FIG. 29

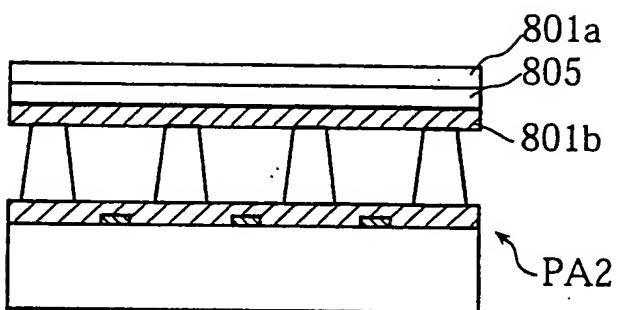




FIG. 30

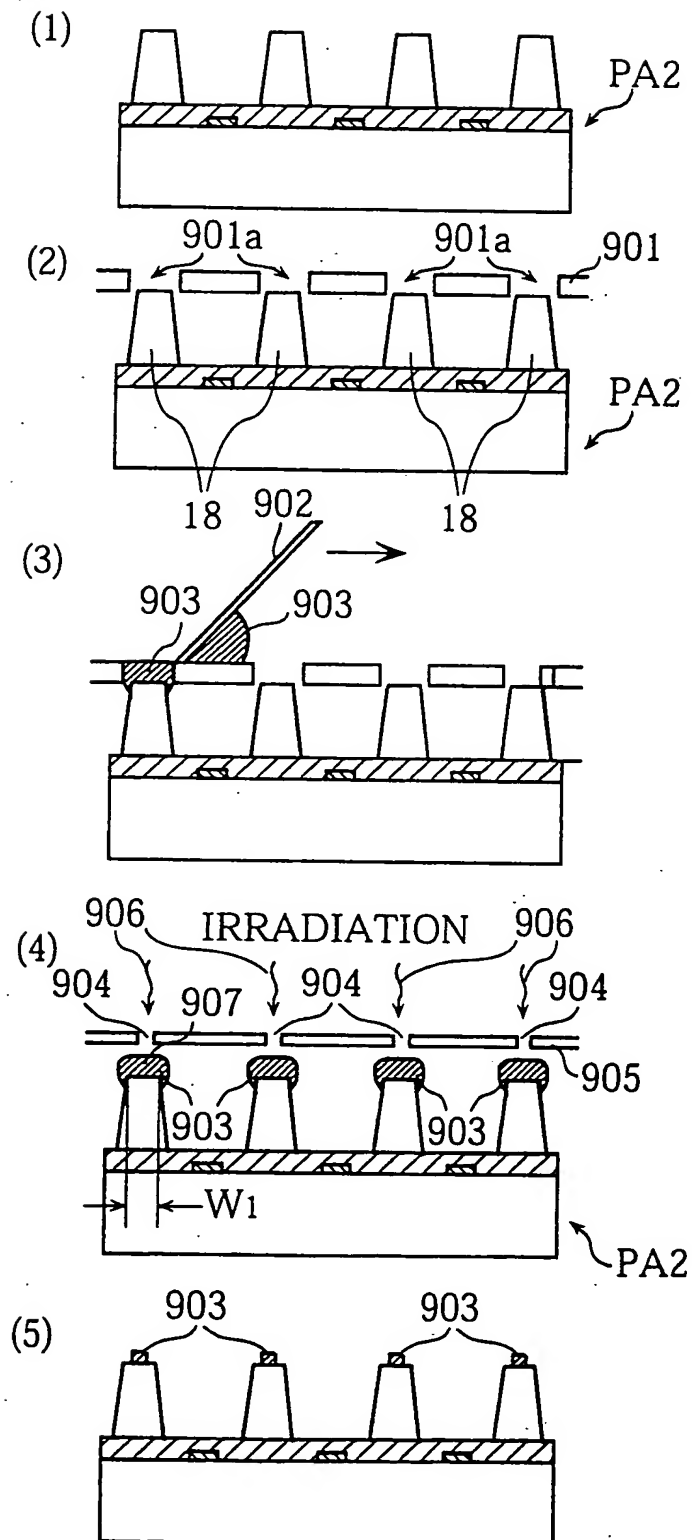




FIG. 31

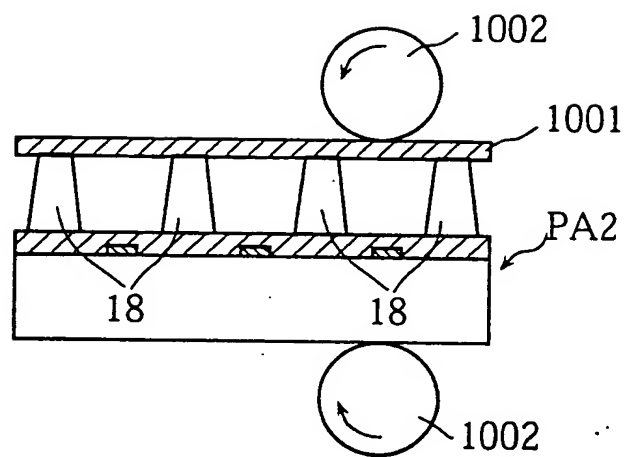




FIG. 32A

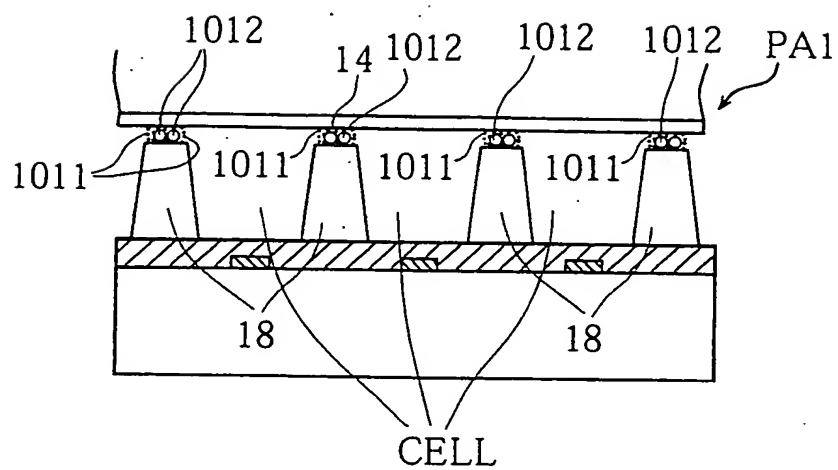


FIG. 32B

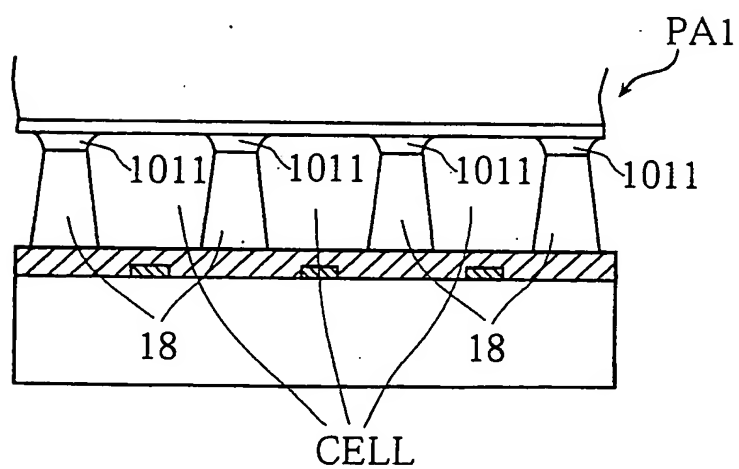




FIG. 33

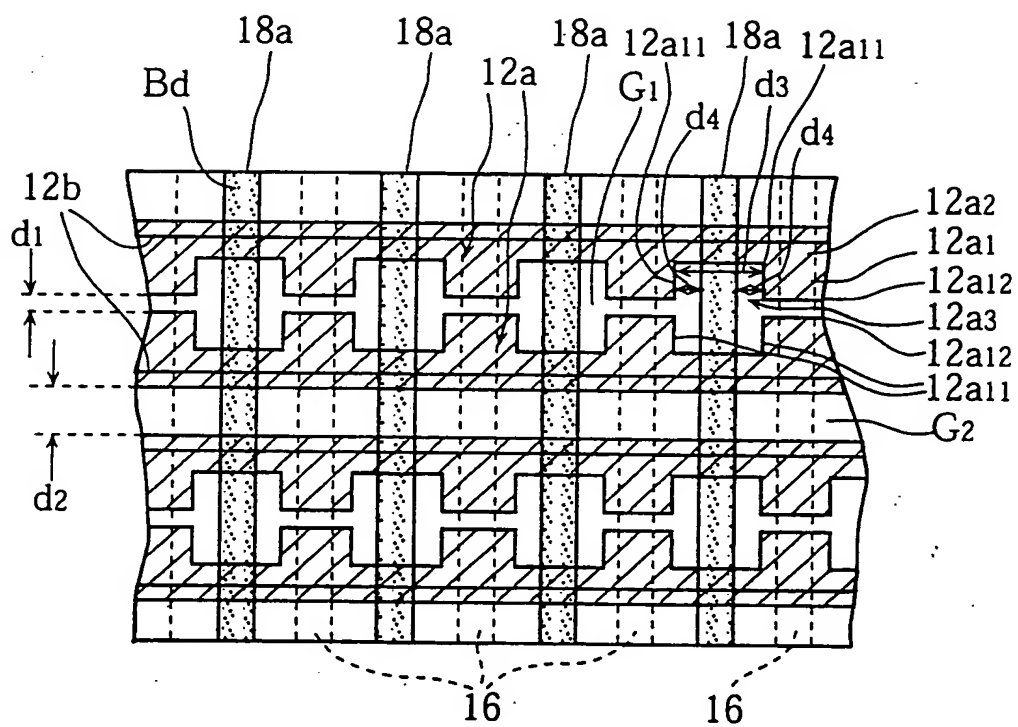




FIG. 34

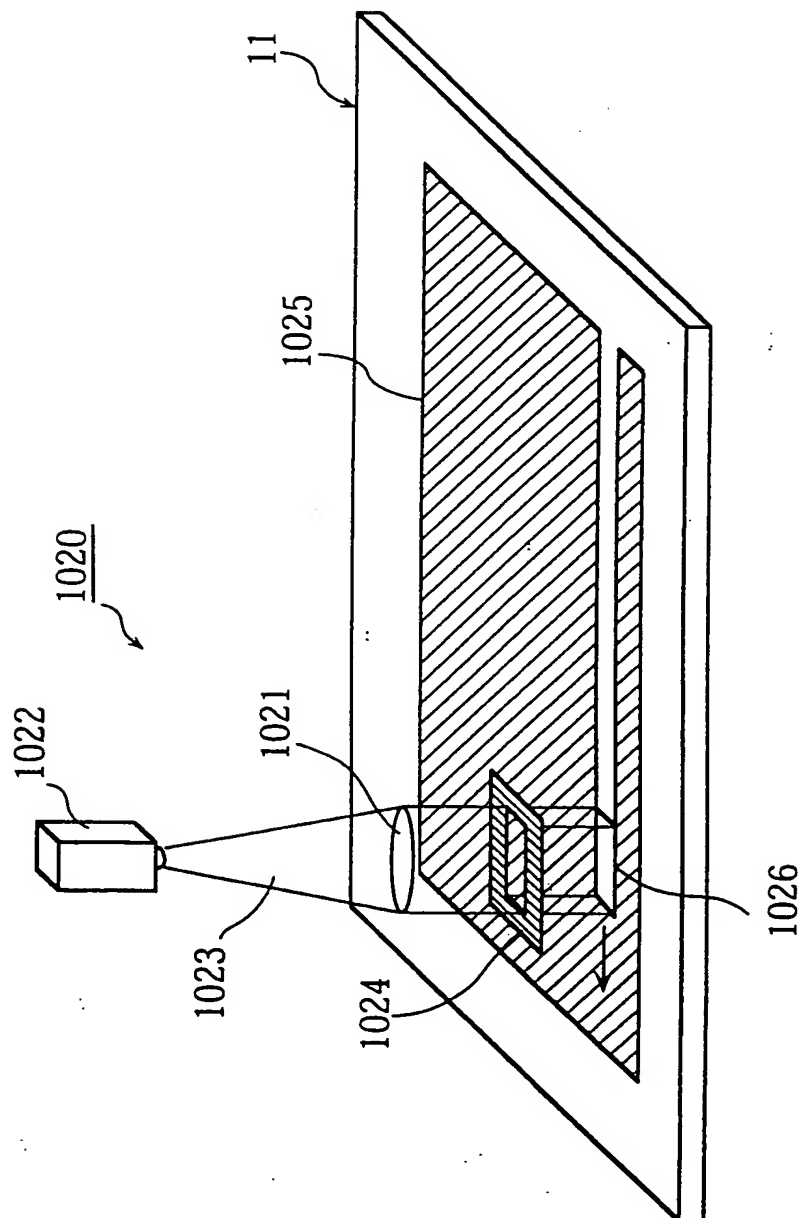




FIG. 1 is a schematic diagram of a display panel 1030. The panel is divided into five vertical columns, each containing four pixels. The pixels are arranged in a grid pattern. The diagram also shows dimensions d1, d2, and d5, and labels 16, 18a, 1030a, 1030a1, 50a1, G1, G2, and 1031.







FIG. 39

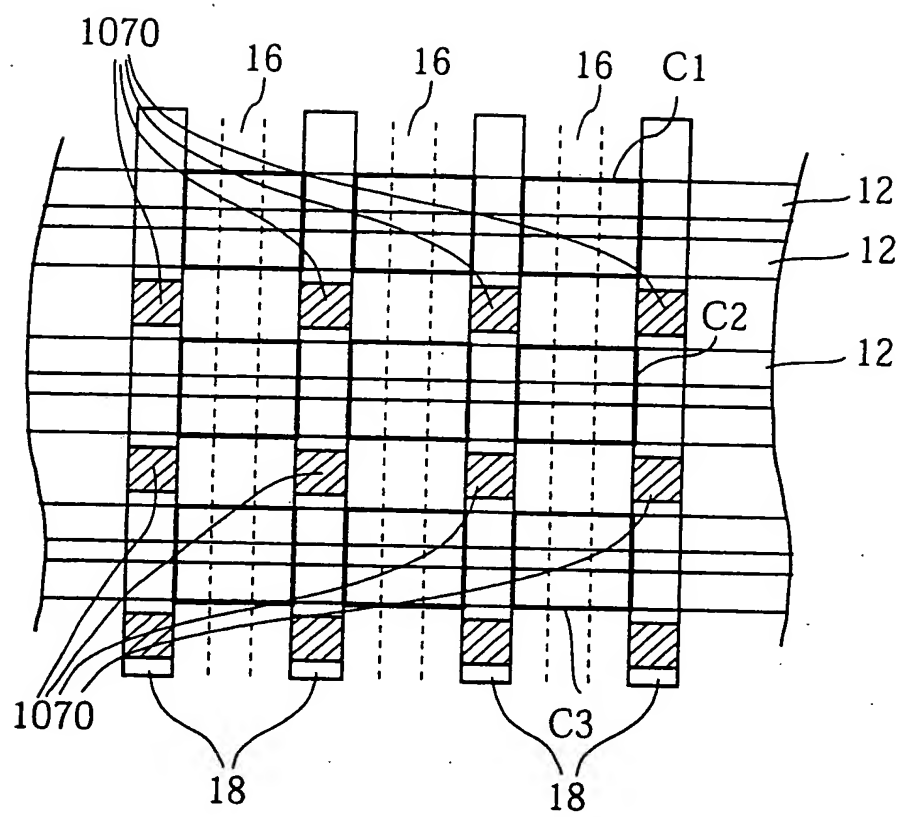




FIG. 40

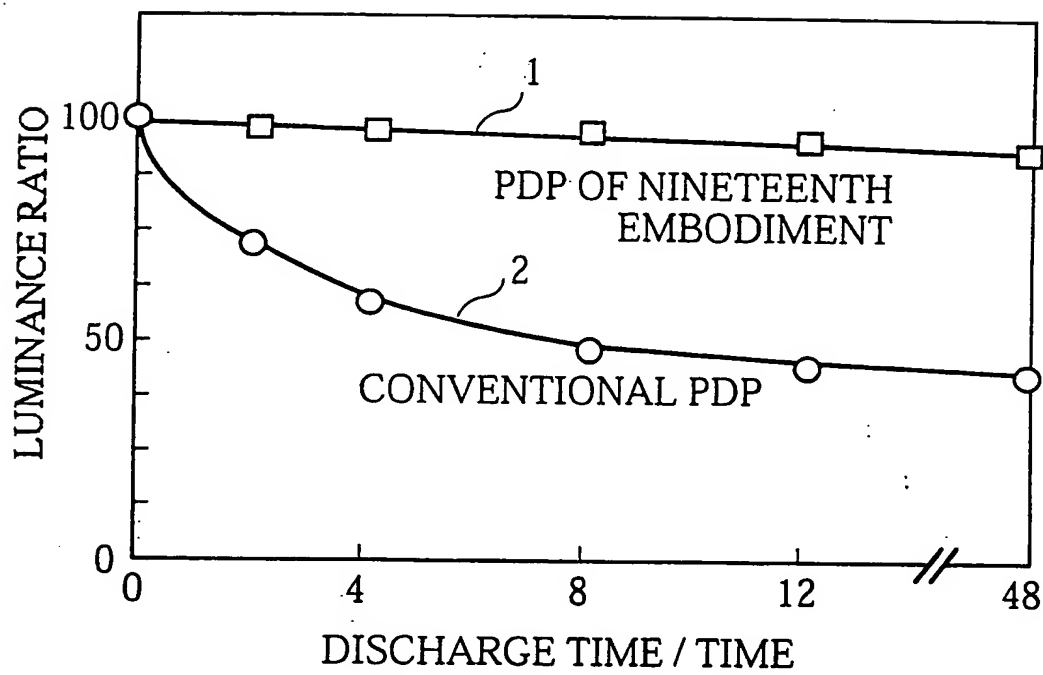




FIG. 41A

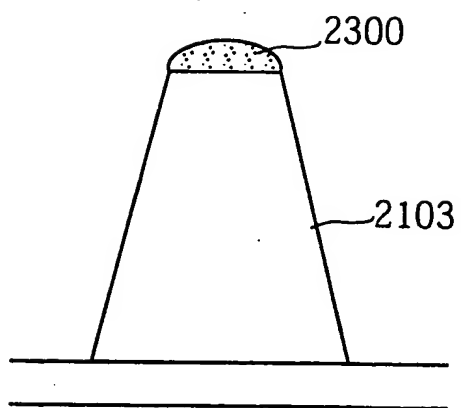


FIG. 41B

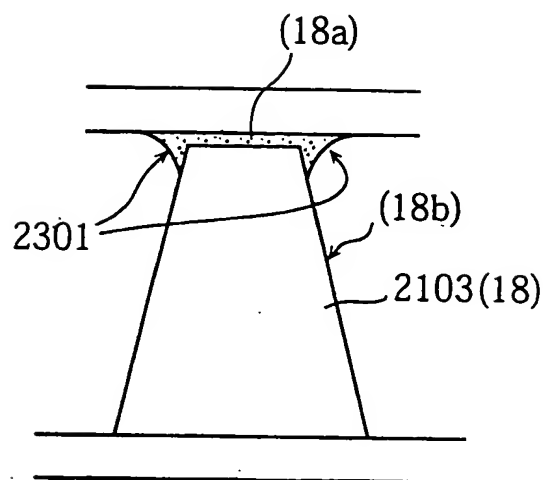




FIG. 42

